

Viktor Verkhovoy
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QUALIFICATION

- Possess experience in analog and digital electronics
- Strong emphasis in analog design and troubleshooting

EDUCATION

Master of Science in Electrical Engineering, Kiev Polytechnic Institute, USSR

- Continuing education including: Spice, C, Protel, Max+Plus, MP-Lab.

EXPERIENCE

Contractor design Engineer Innovative integrated Design, LLC 2002 – present

- DC to DC, A to D, D to A, FPGA (Altera), LVDS, monitors on CRT. Schematic, Board layout, assembly, testing.

R&D Design Engineer Data Ray Corp. Denver, CO 1992- August 2001

- Complete design of various color and monochrome monitors, including multisync monitors with digital control. PLDs: Altera 7064, Altera ACEX 1K30, MaxPlus Synthesis/Simulation. Microchip PICmicro, MP-Lab.
- Specification development, key components verification, schematic capture, board layout, production support, failure analysis.
- Design of video amplifier, deflection circuits, high and low voltage power supply, up and down DC to DC converters, magnetic components
- Coordination of mechanical design of monitor cabinet and chassis

Principal Design Engineer Infracon Research Institute, USSR 1989-1991

- Project leader for design of new components for various computer peripherals

Senior Design Engineer Research Bureau of Information Systems, USSR 1982-1989

- Raster scanning graphic terminal for computer tomograph
- Vector graphic terminal based on storage CRT
- Vector graphic terminal based on penetron color CRT
- Design analog part of devices and coordination of engineering team

Design Engineer Research Bureau of Information Systems, USSR 1974-1982

- Research new components for holographic memory
- Design photo-electron block for registration information on microfilm
- Deflection, digital interpolation
- Digital multimeter for production process control of IC
- Read-Write circuit for magnetic core memory
- Noise resistant counter based on the Hamming's codes
- Digital circuit with applying majority logic